





Advanced Rigid Substrates for HF Applications, Ultra-thin Build-ups and **Chip Packages**

HIGHLIGHTS

- Epoxy based laminates with low dielectric properties
- High Tg, low CTE and high dielectric strength
- Ultra-thin any layer HDIs from 4 layers (~220 μm) to 8 layers (~350 µm) with highest reliability
- Impedance controlled ultra-fine line technology (down to 25 µm)
- High density pitch design (~175 µm)
- Smallest via (50 μm) in pad (100 μm)
- EN 9100:2009 certification



Advanced Rigid Substrates for HF Applications, Ultra-thin Build-ups and Chip Packages

DYCONEX features an advanced rigid multilayer material allowing ultra-thin build-ups for various high frequency applications particularly suitable for HF chip packaging. The material offers low dielectric properties (low Dk and low Df) combined with a low thermal expansion coefficient (CTE) and high dielectric strength.

Using anylayer technology the layers of the substrates are only connected by laser drilled, copper filled microvias allowing for high-density / high-reliability designs. Other HDI build-up solutions are available in addition.

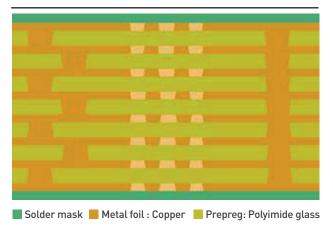
Material Characteristics

Characteristic		Unit	Conditions
CTE	9 – 11	ppm/°C	XY <tg< td=""></tg<>
	20 – 25	ppm/°C	Z <tg< td=""></tg<>
Tg	260-280	°C	DMA
Flexural modulus	25	GPa	25°C
Dielectrical constant	3.5	εr	1 GHz
	3.4	εr	10 GHz
Dissipation factor	0.003		1 GHz
	0.004		10 GHz
Dielectrical strength	160	kv/mm	ASTM D149 Method A

Design Features

Feature	Standard capability	Leading edge capability
Min laser blind via	75 µm	50 µm
Min pad size	150 µm	100 µm
Min pitch	250 µm	175 µm
Copper filled blind vias	all	all
Filling rate blind via	>90 %	>95 %
Final copper thickness	20 - 40 µm	10 - 20 µm
Min lines / spaces	50 - 70 μm	25 - 35 µm
Solder mask thickness	20 ± 10 µm	18 ± 4 μm
Solder mask registration accuracy	± 25 μm	± 15 μm
Min solder mask dam	90µm	75µm
Solder mask color	green / black	green / black
Silkscreen	white / black	white / black
Surfaces	ENIG	ENEPIG/OSP/ISIG/EPIG
Routing	2 mm	0.6 mm

Anylayer Build-Up



Based in Switzerland, DYCONEX has been in the PCB business for more than 50 years and delivers leading edge interconnect solutions in flex, rigid-flex and rigid technology. DYCONEX core competence lies in the production of highly complex HDI, high-frequency and high-reliability circuit boards for medical, defense, aerospace, industrial and semiconductor applications. DYCONEX is an MST company.

Thickness of Ultra-Thin Rigid Build-Ups

Number of layers	Standard capability	Leading edge capability		
2	150 ± 20 μm	100 ± 20 μm		
4	250 ± 25 μm	200 ± 20 μm		
6	340 ± 30 μm	250 ± 25 μm		
8	430 ± 40 μm	300 ± 30 μm		
>8	on request	on request		
TI: 1 11 : 1 11 1				

Thickness measured over all layers incl. solder mask



an MST company

DYCONEX AG Grindelstrasse 40 CH-8303 Bassersdorf, Switzerland Phone +41 (43) 266 11 00 Fax +41 (43) 266 11 01 mail.dyconex@mst.com www.mst.com



Micro Systems Technologies Management GmbH Sieversufer 7-9 DE–12359 Berlin, Germany Phone +49 (30) 68905-4001 info@mst.com www.mst.com

10/2020 All data given in this document are for information purposes only and are not binding. The data are subject to change without notice. All rights reserved by DYCONEX. Copying, even in extracts, is only allowed by licence of DYCONEX.